

PLASMA DISPLAY APPARATUS AND MANUFACTURING METHOD

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ABSTRACT OF THE DISCLOSURE

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A PDP apparatus of low power consumption and without erroneous malfunctions equipped with the sustaining circuit that prevents the on/off timing shift and deterioration of the sustaining pulse, wherein phase adjusting circuits, which adjust the timing of the changing edge of the sustaining pulse, are provided to the sustaining circuit and the power recovery circuit is improved efficiently and the power consumption is reduced by optimizing the timing of the changing edge of the sustaining pulse, has been disclosed. The circuit devices used in the sustaining circuits are classified according to delay times and sets of the circuit devices are selected so that the timing of the changing edge of the sustaining pulse falls within a predetermined allowance, and the selected sets of the circuit devices are set to the PDP.